

VLSI/SOC Design Engineers

We have several immediate entry level job openings for VLSI/ SOC Design Engineers at Intel in Portland, OR. We have openings for

*1)**Digital VLSI/SOC design Engineers *

*2)**Validation Engineers*

*You'll be responsible for definition and implementation of the next-generation Intel SOCs. Typical responsibilities span Micro-Architecture definition, Logic design, Circuit design, chip planning, synthesis, functional/electrical design verification, validation and test development. *

*Requirements for the position include a B.S.(GPA >3.2) or M.S. (GPA >3.5) in electrical/computer engineering or equivalent. Strong fundamental skills in digital hardware design, VLSI circuit design, MOS Devices and Computer architecture are required. Good programming skills will be an advantage. *

*To apply, e-mail your resume to: ****vikram.vemulapalli@intel.com