

## **Samsung Semiconductor, Inc.**

Technical professionals are defined by what they create. Samsung has the risk taking corporate culture, strategic R&D investments and global know-how to imagine, develop and market products that lead the industry. Samsung's Device Lab located in San Jose, CA is currently recruiting world-class engineers who share our "Innovation through passion" philosophy and thrive in a fast-pace, results-driven environment. Ready to re-imagine yourself? Imagine who you can become at Samsung.

### **Intern, Device Research**

Responsibilities will include:

- Perform research on advanced logic and/or memory devices using TCAD simulation
- Closely work with the TCAD development team to propose new direction of model development
- Create and/or evaluate compact models for emerging nanoelectronic devices and perform circuit-level performance benchmark

The qualified candidate will possess the following:

- Strong background in device physics and semiconductor technology is required
- Experience on TCAD simulation and modeling of semiconductor devices is required
- Experience on *ab initio* simulations and/or quantum transport (e.g., NEGF) is highly preferred
- Good knowledge on emerging nanoelectronic devices (e.g., Tunnel FETs, 2D materials, new memory technologies) is highly preferred
- Experience on compact modeling of semiconductor devices is welcome
- Experience on programming in scripting languages (Perl, Python, Tcl) is welcome
- Demonstrated ability to work as a team player, as part of a local and globally distributed team
- Good oral and written skills for communication and documentation
- MS or Ph.D. degree in EE or related fields (e.g., applied physics, material science) or currently enrolled in such a program

Email resume to [collegeresume@ssi.samsung.com](mailto:collegeresume@ssi.samsung.com)