



RAMP Device Engineer

We are thousands of people working globally to help the world access, analyze and act on ever-increasing volumes of data, creating the foundation for new applications and industries. We see the potential for a world that's more predictive, more productive, more personal. Our products and solutions marketed under the WD, HGST and SanDisk brands, are everywhere that digital information and content is found: in the cloud, supporting your mobile digital lifestyle; in business and personal computers; in external storage devices; in your digital video recorder and cameras. If you have a relentless drive to deliver, if you want to work in an environment where people and ideas thrive, come be part of the team that's transforming the world's connection to information.

General Description

Memory Technology is responsible for developing our technology roadmap based on current and future capabilities while considering market trends, customer roadmaps, new opportunities and potential solutions. Technology manages our core memory, device and process technologies. The organization also oversees integration of memory into major product platforms and develops emerging technologies and platforms, including new product architectures.

Responsibilities and Project Scope

- In this position, the individual will be responsible for development of the future generation of 3D NAND flash.
- The work will involve electrical characterizations of flash memory chips with the goal of improving the Reliability & Performance of the Memory Cell.
- The individual will perform measurements of cell reliability and performance, compare it across devices from different process conditions followed by statistical data analysis.
- The individual will also work at root cause analysis of cell reliability issues and identifying their solutions.

Minimum Qualifications

- Western Digital defines Intern candidates as individuals who are currently enrolled in accredited degree program and is due to graduate in December 2017 or later.
- Advanced degree, Bachelors, Masters or Ph.D in Electrical Engineering, Material Science, Chemistry, or Physics

Preferred Skills

- It requires in-depth knowledge of semiconductor device physics and/or nanoelectronics, familiarity with advanced semiconductor processing technologies, and knowledge in statistical methods for experimental design and data analysis
- The ideal individual must have proven ability to achieve results in a fast moving, dynamic environment
- The individual must be self-motivated and self-directed, however, must have also demonstrated ability to work well with people and be an efficient team player.

All qualified applicants will receive consideration for employment without regard to race, sex, color, religion, sexual orientation, gender identity, national origin, protected veteran status, or on the basis of disability.

- In addition, the individual must have abilities to work cross-functionally, troubleshoot, analyze and solve complex problems, as well as manage multiple projects simultaneously while meeting deadlines
- Familiarity with coding languages (such as Python and Perl) and statistical analysis software (such as Spotfire, Jump) is an advantage