

## Job Openings for UCLA Students



**Analog Bits, Inc. is the leading supplier of low-power, customized clocking and interconnect IP for easy and reliable integration into modern CMOS digital chips.**

Our product lines include precision clocking macros such as PLLs & DLLs, programmable interconnect solutions such as multi-protocol SERDES/PMA and programmable I/Os, and specialized memories including high-speed SRAMs and T-CAMs.

With billions of IP fabricated into customer silicon from 0.35u to 28-nm processes, Analog Bits is the premier analog IP supplier with an outstanding heritage of "first time working silicon" at merchant foundries and IDMs.

We design, license and support our products to a global customer base and looking for CMOS circuit designers who have a passion for circuits and the ability to take on immediate product responsibilities

### **Entry Level CMOS Mixed Signal Circuit Design Engineer (Multiple Openings)**

#### **Job Duties:**

- Design and develop full-custom transistor-level design blocks optimized for performance, power and area
- Analyze and customize circuits for use within high-precision PLLs and DLLs, SERDES Transceivers, T-CAMs, and specialized I/Os such as high speed DDR
- Implement circuits at gate and transistor-level, and then verify performance and function using simulations
- Define, generate or utilize circuits, schematics and Spice/Verilog models
- Perform extensive spice simulations including Monte Carlo analysis
- Collaborate with layout team and supervise layout implementation
- Characterize/evaluate silicon in the lab
- Support customers' engineering groups with both pre-sales and post delivery

**Job Requirements:** The successful candidate will have a good understanding of CMOS circuits, be highly motivated, and quick to adopt new technologies. Exceptional interpersonal, time management and communication skills are critical for working with engineering and product development groups. The successful candidate will have the ability to multi-task ensuring timely completion of several complex independent tasks.

- BS or MS in Electrical Engineering for entry level or 2 to 5 years' experience for senior level
- Experience in using high-speed circuit simulators such as HSPICE, Finesim,
- Must have good written and oral communication skills and ability to work independently.