



## FPGA Engineer (Intern)

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Samsung Semiconductor, Inc. is a world leader in Memory and Storage technologies. We are currently looking for a FPGA Engineer (Intern) to join our team in San Jose, CA. The FPGA Engineer Intern will contribute memory and storage system research in the Memory Solutions Lab. He or she will join a team of experts in researching and developing innovative memory and storage system solutions that utilize existing and emerging technologies to add substantial value to storage systems. The ideal candidate must have a strong understanding of computer architecture and storage systems.

### Responsibilities

- Implement data intensive applications on Xilinx Virtex FPGA development board.
- Compare the performances in different platforms.
- Analyze performance bottleneck and propose new architecture and storage systems for more efficient computation.
- Work with team members to contribute towards prototyping efforts.
- Create new and useful IP, publish at conferences, and generate whitepapers.

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### Background/ Experience

- Pursuing an MS or PhD in Computer Science, Computer Engineering or related field, with focus on Computer Architecture and Systems.
- Good knowledge of FPGA.
- Research and development experience with FPGA.
- Prior experience with performance analysis and optimization will be a big plus.
- Track record of innovation and creativity in problem solving.

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### Skills

- Must be highly motivated with excellent verbal and written communication skills.
- Understanding of image recognition.
- Strong background in C/C++, Verilog/VHDL programming. Knowledge of CAPI is a big plus.
- Comfortable working in a multinational environment and understands how to leverage cultural diversity.
- Inherent technical curiosity

Email resume to [collegeresume@ssi.samsung.com](mailto:collegeresume@ssi.samsung.com)