Altera Corp.

Position: Software Engineer (Bruinview req# 794367)

Description: Altera's cutting edge FPGAs and HardCopy ASICs require increasingly complex software. You are a Software Engineer who wants to build key components of this software, are excited by learning about new technologies, and are motivated by being part of a team that is critical to the success of Altera. The ideal candidate should enjoy working closely with code, customers, and colleagues alike.

This position is located at Altera's headquarters San Jose, California. Experience the excitement of living and working in the heart of Silicon Valley, surrounded by the top tech companies in the world, while enjoying the great California weather.

Qualifications:
The successful candidate's minimum qualifications will include the following:
- BS in Computer Science, Electrical Engineering, Computer Engineering, or equivalent.
- Experience with scripting languages such as Perl and Tcl
- Extensive experience with large code bases in C and/or C++, (Python and/or Java.)
- Knowledge of object-oriented programming, data structures and algorithms.
- Strong background in data-structure and algorithm implementation in C/C++

Job2 Position: Design Engineer (Bruinview req# 794368)

Description: Altera's cutting edge FPGAs and HardCopy ASICs require increasingly complex software. You are a Design Engineer who wants to build key components of this software, are excited by learning about new technologies, and are motivated by being part of a team that is critical to the success of Altera. The ideal candidate should enjoy working closely with code, customers, and colleagues alike.

This position is located at Altera's headquarters San Jose, California. Experience the excitement of living and working in the heart of Silicon Valley, surrounded by the top tech companies in the world, while enjoying the great California weather.

Qualification:
The successful candidate's minimum qualifications will include the following:
- BS in Computer Engineering, Electrical Engineering or equivalent
- Strong understanding of RTL coding principles including proficiency in Verilog/VHDL is required.
- Proficiency in scripting languages (Perl, TCL) is also expected.
- Some understanding of HW validation is also expected.
- Some understanding of transceivers and FPGA architecture is also expected

*** Interested students can submit their resume on Bruinview with the corresponding Job req# to be considered for on-campus interview or you can send your resume to me at smok@altera.com<mailto:smok@altera.com>

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Software and IP Engineering
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