FPGA Development Job Availability
Development of Novel Methods for Cancer Diagnosis and Therapy
Photonics Laboratory (http://www.photonics.ucla.edu) led by Prof. Bahram Jalali in the Electrical Engineering Department at UCLA seeks an undergraduate or graduate student with experience in FPGA development. The goal of the project is to develop novel methods for cancer diagnosis and therapy. The job involves development on Virtex 6 and 7 FPGA boards to provide real-time image processing for UCLA’s STEAM imager (the world’s fastest imaging system). The system is being developed for early stage cancer detection via imaging of individual blood cells. The STEAM technology has the potential to revolutionize cancer diagnosis and treatment. The primary qualification is prior experience with FPGA development. Extensive knowledge of Verilog RTL coding is required. Experience with image processing is a plus, but not required. Graduate students are preferred. However, the position is open to other qualified candidates. A competitive salary guaranteed.
Interested candidate should send a brief e-mail message and resume to Jagannath Sadasivam (jagannath.s3@gmail.com)