Job Title: Intern - DSP  
Requisition #: 6475  
Group: Engineering and Technology Group  
Department: RECONFIGURABLE TRANSCEIVER SYSTEM DEV  
Clearance Required: Secret  

**Duties:**

- Participate in the development of digital communication and navigation proof of concept and test bed systems  
- Digital hardware development and testing  
- System-level RF design and receiver software development in C/C++  
- Real time embedded software development  
- FPGA implementation  
- Python coding, GI development, laboratory testing and measurement  
- Work will involve close contact with department staff

**Qualifications:**

**EDUCATION**

- Progression towards BS or higher in EE or related discipline is required.

**REQUIRED EXPERIENCE**

- Laboratory experience

**PREFERENCE GIVEN TO CANDIDATES WITH EDUCATION AND/OR EXPERIENCE IN:**

- Digital signal processing, digital design  
- Embedded SW development, GNU radio development, VHDL coding  
- C/C++ development, Python experience  
- Experience or class work involving embedded programming on an ARM processor in Linux or other RTOS  
- Candidate must also have good written and verbal communication skills.